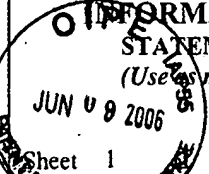


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> 	Application Number: 10/034,565
	Confirmation No.: 2587
	Filing Date: October 19, 2001
	First Named Inventor: Hugo A. Andrade
	Art Unit: 2123
	Examiner Name: Osborne, Luke R.
	Attorney Docket Number: 5150-22503

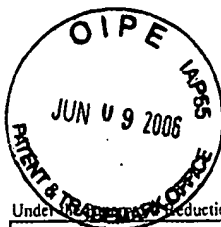
U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
ho		US - 5,309,556	5/3/1994	Robert C. Sismilich	
		US - 5,541,849	7/30/1996	Rostoker, et al.	
		US - 5,638,299	6/10/1997	Keith Miller	
		US - 5,732,277	3/24/1998	Kodosky, et al.	
		US - 5,737,235	4/7/1998	Kean et al.	
ho		US - 6,064,409	5/16/2000	Thomsen, et al.	
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code-Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	Check if English Translation is attached
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, (page(s), volume-issue number(s), publisher, city and/or country where published.	Check if English Translation is attached
ho	B1	MARLEEN ADE, RUDY LAUWEREINS and J.A. PEPERSTRAETE; "Hardware-Software Codesign with Grape"; Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, June 1995; pages 40-47.	<input type="checkbox"/>
ho	B2	CHRISTIAN BOULAY, MICHEL LAFORTUNE and ROBERT GUARDO; "A High Throughput Controller for a 256-channel Cardiac Potential Mapping System"; Canadian Conference on Electrical and Computer Engineering; Sept. 1995; pages 539-542; Vol. 1.	<input type="checkbox"/>

Examiner Signature		Date Considered	8/17/06
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Sheet 2 Of 2	Attorney Docket Number: 5150-22503

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, (page(s), volume-issue number(s), publisher, city and/or country where published.	Check if English Translation is attached
	B3	L. COLLAMATI, F. FILIPPETTI, G. FRANCESCHINI, S. PIRANI and C. TASSONI; "Induction Machine Stator Fault On-line Diagnosis Based on LabVIEW Environment"; Mediterranean Electrotechnical Conference; May 1996; pages 495-498; Vol. 1.	<input type="checkbox"/>
	B4	LUC DE COSTER; "Grape-II: An Introduction" [online]; Automatic Control and Computer Architectures Department; Katholieke Universiteit Leuven, Belgium, Feb. 22, 1996 Retrieved from the Internet: http://www.esat.kuleuven.ac.be/acca . 25 pages	<input type="checkbox"/>
	B5	RUDY LAUWEREINS, MARC ENGELS MARLEEN ADE and J.A. PEPERSTRAETE; "Grape-II: A System-Level Prototyping Environment for DSP Applications"; Computer; Feb. 1995; pages 35-43; Vol. 28, Issue 2.	<input type="checkbox"/>
	B6	PATRICK LYSAGHT and JON STOCKWOOD; "A Simulation Tool for Dynamically Reconfigurable Field Programmable Gate Arrays"; IEEE Transactions on Very Large Scale Integration Systems; Sept. 1996; pages 381-390; Vol. 4, Issue 3.	<input type="checkbox"/>
	B7	MICHAEL PETRONINO, RAY BAMBHA, JAMES CARSWELL and WAYNE BURLESON; "An FPGA-base Data Acquisition System for a 95 GHz. W-band Radar"; IEEE International Conference on Acoustics, Speech and Signal Processing; April. 1997; pages 4105-4108; Vol. 5.	<input type="checkbox"/>
	B8	H.J.W. SPOELDER, A.H. ULLINGS and F.C.A. GROEN; "Virtual Instrumentation: A Survey of Standards and their Interrelation", IEEE Technology Conference Instrumentation and Measurement; May 1997; pages 676-681, Vol. 1.	<input type="checkbox"/>
	B9	SUJATHA SRINIVASAN, M. BODRUZZAMAN, A. SHIRKHODAIE and M. MALKANI; "LabVIEW program Design for On-Line Data Acquisition and Predictive Maintenance", Proc. of the 30th Southeastern Symp. on System Theory, March 1998; pages 520-524.	<input type="checkbox"/>
	B10	R.S.D. WAHIDABANU, M.A. PANNEER SELVAM and K. UDAYA KUMAR; "Virtual Instrumentation with Graphical Programming for Enhanced Detection & Monitoring of Partial Discharges"; Electrical Insulation Conference Sept. 1997; pages 291-296.	<input type="checkbox"/>
			<input type="checkbox"/>
			<input type="checkbox"/>

Examiner Signature		Date Considered	8/17/06
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